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Attorney Docket No.:

NSC1P194/P04836

First Named Inventor:

MOSTAFAZADEH

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09/698736  
10/26/00

**UTILITY PATENT APPLICATION TRANSMITTAL (37 CFR. § 1.53(b))**

Box Patent Application  
Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir: This is a request for filing a patent application under 37 CFR. § 1.53(b) in the name of inventors:  
**SHAHAM MOSTAFAZADEH**

For: **CHIP SCALE PIN ARRAY**

**Application Elements:**

- ☒ 16 Pages of Specification, Claims and Abstract
- ☒ 10 Sheets of Formal Drawings
- ☒ 2 Pages Combined Declaration and Power of Attorney

**Accompanying Application Parts:**

- ☒ Assignment and Assignment Recordation Cover Sheet (recording fee of \$40.00 enclosed)
- ☐ 37 CFR 3.73(b) Statement by Assignee
- ☐ Information Disclosure Statement with Form PTO-1449
- ☐ Copies of IDS Citations
- ☐ Preliminary Amendment
- ☒ Return Receipt Postcard
- ☐ Small Entity Statement(s)
- ☐ Other:

Calculation (37 CFR § 1.16)

	(Col. 1) Total Claims		(Col. 2) Claims	(Col. 3) Present Extra	Rate	Additional Fee
TOTAL	20	MINUS	20	= 0	x 18 =	0
INDEP.	2	MINUS	3	= 0	x 80 =	0
[ ] First presentation of multiple dependent claim					\$270 =	0
Basic Filing Fee under 37 C.F.R. §1.16(a)					\$710 =	\$710.00
TOTAL						\$710.00
SMALL ENTITY 50% FILING FEE REDUCTION (if applicable)						N/A

☒ Check No. 1533 in the amount of \$710.00 is enclosed in payment of basic filing fee. Check No. 1534 in the amount of \$40.00 is enclosed in payment of assignment recordation fee.

☒ The Commissioner is authorized to charge any fees beyond the amount enclosed which may be required, or to credit any overpayment, to Deposit Account No. 500388 (Order No. NSC1P194).

General Authorization for Petition for Extension of Time (37 CFR §1.136)

☒ Applicants hereby make and generally authorize any Petitions for Extensions of Time as may be needed for any subsequent filings. The Commissioner is also authorized to charge any extension fees under 37 CFR §1.17 as may be needed to Deposit Account No. 500388 (Order No. NSC1P194).

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## CHIP SCALE PIN ARRAY

### RELATED APPLICATIONS

5           This application is related to the commonly assigned Application No.:09/590,551 entitled "LEAD FRAME DESIGN FOR CHIP SCALE PACKAGE," filed on June 9, 2000 by Shahram Mostafazadeh, and incorporated herein by reference.

          This application is related to the commonly assigned Application  
No.: \_\_\_\_\_ (Attorney Docket No.: NSC1P195 ) entitled "FLIP CHIP SCALE  
10   PACKAGE," filed on even date by Shahram Mostafazadeh herewith and incorporated herein by reference.

### FIELD OF THE INVENTION

          The present invention relates generally to integrated circuit packages. More specifically, the invention relates to chip scale integrated circuit packages.

### 15           BACKGROUND OF THE INVENTION

          An integrated circuit (IC) package encapsulates an IC chip (die) in a protective casing and may also provide power and signal distribution between the IC chip and an external printed circuit board (PCB). An IC package may use a metal lead frame to provide electrical paths for that distribution.

20           To facilitate discussion, FIG. 1 is a top view of a lead frame panel 100 made up for a plurality of lead frames that may be used in the prior art. The lead frame may comprise leads 108, die attach pads 112, ties 116 for supporting the die attach pads 112, and a skirt 120 for supporting the plurality of leads 108 and ties 116. The lead frame panel 100 may be etched or stamped from a thin sheet of metal. IC chips 124 may be mounted to the die attach pads 112  
25   by an adhesive epoxy. Wire bonds 128, typically of fine gold wire, may then be added to electrically connect the IC chips 124 to the leads 108. Each IC chip 124 may then be

encapsulated with part of the leads 108 and the die attach pad 112 in a protective casing, which may be produced by installing a preformed plastic or ceramic housing around each IC chip or by dispensing and molding a layer of encapsulating material over all IC chips 124. FIG. 2 is a cross-sectional view of part of the lead frame panel 100 and IC chips 124. In a process described in U.S. Patent Application Number 09/054,422, entitled "Lead Frame Chip Scale Package", by Shahram Mostafazadeh et al., filed April 2, 1998, a tape 136 is placed across the bottom of the lead frame panel 100 and an encapsulating process is used to encapsulate the IC chips 124, the wire bonds 128, and part of the lead frame panel 100. The tape 136 prevents the encapsulating material 140 from passing through the lead frame panel 100. Once the encapsulating material 140 is hardened, the tape 136 may be removed. The encapsulating material 140 may be cut to singulate the IC chips 124 and leads 108. The resulting singulated IC chips have leads with a length 155 which is substantially parallel to the conductive surface of the IC chips 124.

It is desirable to provide an IC package process which does not require the steps of creating wire bonds between the die and leads, adding tape to the lead frame, and then removing the tape from the lead frame. It is also desirable to provide a process and lead frame that provides lead fingers and a chip scale footprint.

#### SUMMARY OF THE INVENTION

To achieve the foregoing and other objects and in accordance with the purpose of the present invention, the invention provides an integrated circuit package. The integrated circuit package comprises a first die with a conductive side, a plurality of lead posts where the conductive side of the first die faces the plurality of lead posts, and an encapsulating material encapsulating the first die and an end of the lead posts adjacent to the conductive side of the die.

Another aspect of the invention provides a method for packaging integrated circuits. Generally, a lead frame of a conductive material with a plurality of lead posts and a connecting sheet connecting the plurality of lead posts is provided. A plurality of first dice is attached to the lead frame, wherein each first die is electrically and mechanically connected to a plurality of the plurality of lead posts, and wherein a conductive side of each first die faces

the plurality of lead posts. The plurality of dice is then encapsulated with an encapsulating material.

These and other features of the present invention will be described in more detail below in the detailed description of the invention and in conjunction with the following figures.

#### BRIEF DESCRIPTION OF THE DRAWINGS

The present invention is illustrated by way of example, and not by way of limitation, in the figures of the accompanying drawings and in which like reference numerals refer to similar elements and in which:

FIG. 1 is a top view of a lead frame and die assembly used in the prior art.

FIG. 2 is a cross-sectional view of part of the lead frame panel illustrated in FIG. 1.

FIG. 3 is a flow chart of a process used in a preferred embodiment of the invention.

FIG. 4 is a top view of part of a metal sheet, which is formed to a lead frame.

FIG. 5 is a cross-sectional view of part of the lead frame.

FIG. 6 is a detailed flow chart of one embodiment of the step of attaching the dice to the lead frame.

FIG. 7 is a cross-sectional view of a lead frame with conductive epoxy and with dice.

FIG. 8 is a cross-sectional view of encapsulated dice mounted on the lead frame.

FIG. 9 is a cross-sectional view of a singulated integrated circuit package.

FIG. 10 is a flow chart of the forming of the singulated dice in another embodiment of the invention.

FIG. 11 is a top view of a wafer with a plurality of bumped dice.

FIG. 12 is a side view of a singulated die with solder bumps attached to conductive pads.

FIG. 13 is a detailed flow chart of the step of attaching the singulated bumped dice to the metal frame.

FIG. 14 is a cross-sectional view of a lead frame with bumped dice attached.

FIG. 15 is a cross-sectional view of part of the lead frame and dice after  
5 encapsulation.

FIG. 16 is a cross-sectional view of a resulting IC package with leads.

FIG. 17 is a flow chart of a method of making another embodiment of the invention.

FIG. 18 is a cross-sectional view of a lead frame and dice after encapsulation.

FIG. 19 is a cross-sectional view of a resulting IC package.

#### DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

The present invention will now be described in detail with reference to a few preferred embodiments thereof as illustrated in the accompanying drawings. In the following description, numerous specific details are set forth in order to provide a  
10 thorough understanding of the present invention. It will be apparent, however, to one skilled in the art, that the present invention may be practiced without some or all of these specific details. In other instances, well-known process steps and/or structures have not been described in detail in order to not unnecessarily obscure the present invention.

To facilitate discussion, FIG. 3 is a flow chart of a method used in a preferred  
20 embodiment of the invention. A lead frame is formed (step 304). FIG. 4 is a top view of part of a metal sheet, which is formed to a lead frame 400. The lead frame 400 is preferably copper; however, other electrically conductive materials, such as Alloy 42, may be used instead. FIG. 5 is a cross-sectional view of part of the lead frame 400. The lead frame 400 may be formed by stamping or etching a sheet of metal. The lead frame 400  
25 provides a plurality of posts 404. In this embodiment, the posts 404 all have the same cross-section. In addition, the posts 404 are spaced apart to form an array. The array is formed by aligning posts vertically and horizontally in columns and rows, so that the

vertical spacing is equal to the horizontal spacing. In other embodiments the spacing and location of the posts may vary to meet specific requirements. As illustrated, in this embodiment a connecting sheet 408 extends through the lead frame 400 so that the connecting sheet 408 is connected to an end of each of the posts 404. The connecting sheet 408 is imperforate in that it extends between the posts 404 without any apertures. Plating, such as silver plating, may be added to the lead frame 400. Much of the packaging handling equipment is designed for handling lead frame strips; therefore, the part of the lead frame shown may be a part of a lead frame strip. Such a lead frame strip may provide a skirt surrounding several connecting sheets with posts. Such connecting sheets would be imperforate with the skirt having apertures to relieve stress. Alternatively, the part of the lead frame shown may be part of a square or rectangular panel.

A plurality of singulated dice is formed (step 308). The dice are then attached to the lead frame 400 (step 312). FIG. 6 is a detailed flow chart of one embodiment of the step of attaching the dice to the lead frame. In this embodiment, a conductive epoxy 704 is placed on an upper surface of the posts 404 of the lead frame 400 (step 604), as shown in FIG. 7. A roller may be used to place the epoxy on the upper surface of the posts 404. To place the conductive pads of the dice in the conductive epoxy (step 608), dice 708 are placed over the posts 404 with the side of the dice having conductive pads 712 closest to the conductive epoxy 704, as shown in FIG. 7. The dice 708 are electrically and mechanically mounted on upper parts of the posts 404 by placing the conductive pads 712 in the conductive epoxy 704 so that the conductive side of each die is facing the posts 404, as shown in FIG. 8. The epoxy 704 may be regular conductive epoxy, which is applied just before the dice are placed. In the alternative, the epoxy may be a conductive B stage epoxy, which is applied and partially cured. After the dice are placed, the conductive stage B epoxy is reheated to melt the epoxy. Once the dice 708 are mounted, the conductive epoxy 704 may be dried and cured (step 612). The conductive pads 712 are on the conductive side of the dice, so that the conductive side of the dice is the closest part of each die to the posts.

The dice 708 may then be encapsulated (step 316). Using conventional molding the lead frame 400 can be encapsulated by an encapsulating material 804, as shown in FIG. 8. If a dispensing method is used, then a dam may be placed around the lead frame

400. Since the connecting sheet 408 is imperforate in that it extends between the posts without any apertures, the connecting sheet prevents the encapsulating material from flowing under the lead frame 400, thus keeping the encapsulating material on one side of the lead frame 400. The encapsulating material may then be hardened and cured to form a cap. One cap may be used to cover the entire lead frame. In the alternative, several caps may be provided at various parts of the lead frame. In this embodiment, the posts 404 have curved sides 808 to provide additional locking of the encapsulating material 804 to the posts 404.

The lead frame 400, dice 708, and encapsulating material 804 may be mounted on sticky tape and then placed on a vacuum chuck of sawing equipment, which is known in the art. The sawing equipment may be used for removing all or parts of the connecting sheet 408 to electrically isolate the posts 440 (step 320). In such a removal process, the sawing equipment may cut through only the connecting sheet 408 between the plurality of posts 404. The removal of parts of the connecting sheet forms the posts 404 into lead fingers 904, which may be electrically isolated from each other, as shown in FIG. 9. Etching may be used in the alternative to make isolated lead fingers. In the alternative, the connecting sheet may be polished off. Since the IC packages have not been singulated, the IC packages are in one piece held together by the encapsulating material 804. This allows all IC packages to be tested in panel form (step 324), which allows faster and easier testing. After testing, the saw equipment may then be used to singulate the lead frame 400, dice 708, and encapsulating material 804 into individual IC packages 908 (step 328), as shown in FIG. 9. In such a singulation process, the sawing equipment cuts through the encapsulating material 804.

If panel testing is not desired, then the chip scale IC packages may be singulated before electrical testing. In such a case, singulation may be performed during the step of removing the connection sheet. In other embodiments, other methods besides using saw equipment, such as etching or laser cutting, may be used to remove the connection sheet and/or for singulation.

The lead fingers 904 or lead posts 404 have a length "l" which extends from the top of the posts 404 to the bottom of the posts, wherein the length "l" is substantially perpendicular to the conductive surface of the die, as shown. The length "l" of the lead



fingers 904 or lead posts 404 is more perpendicular to the conductive surfaces of the dice than parallel to the conductive surfaces as was shown in the description of the prior art. The resulting IC package has a footprint on the order of the footprint of the die, as shown. Since the lead fingers are on only a single side of the die, which is the conductive side of the die, and since the length of the lead fingers is perpendicular to the conductive surface of the die, the footprint of the lead fingers is about equal to the footprint of the die, as shown. The conductive epoxy provides both a mechanical connection between the die and lead frame and an electrical connection between the conducting pads and the posts of the lead frame. The frame allows dice of different sizes and different input/output counts may be accommodated. In some embodiments, all of the connecting sheets are removed and in other embodiments at least part of the connecting sheet is removed so that the posts may be formed into electrically isolated lead fingers. No wire bonding is required in this embodiment, thus eliminating a processing step.

In another embodiment of the invention, using FIG. 3 as a high-level flow chart, a lead frame is formed (step 304). The lead frame may be the same lead frame shown in FIG.'s 4 and 5. Singulated dice are also formed (step 308). FIG. 10 is a flow chart of the forming of the singulated dice in this embodiment of the invention. First dice are formed on a wafer (step 1004). FIG. 11 is a top view of a wafer 1100 with a plurality of dice 1102. Bumps of conductive material, such as solder bumps 1104, are formed on conductive pads of the dice 1102 (step 1008). The wafer 1100 is then cut to singulate the bumped dice (step 1012). In other embodiments, these steps may be done in a different order, such as singulating the dice before adding the bumps of conductive material. FIG. 12 is a side view of a singulated die 1204 with solder bumps 1208 attached to conductive pads (attach pads) 1212. The conductive pads are on the conductive side of each die.

The singulated bumped dice are then attached to the lead frame (step 312). FIG. 13 is a detailed flow chart of the step of attaching the singulated bumped dice to the lead frame. The dice 1204 are placed on the lead frame 1404 so that the solder bumps 1208 are placed on an upper surface of the posts 1408, as shown in FIG. 14. Since the solder bumps are on the conductive side of each die, the conductive side of each die is closest to the posts. The solder bumps 1208 are reflowed (1308), which comprises softening the solder possibly by heating so that the solder bumps 1208 become attached to the posts 1408. The solder electrically and mechanically connects the conductive pads to the posts.

A solder past may be added to the top of the posts before the bumped dice are placed. In the alternative, a solder paste may be added to the top of the post and dice that are not bumped may be used.

5 The dice 1204 may then be encapsulated (step 316). Using conventional molding, the dice 1204 can be encapsulated by encapsulating material 1504, as shown in FIG. 15. If a dispensing method is used, then a dam may be placed around the lead frame 1404. The connecting sheet 1412 helps to prevent the encapsulating material from flowing under the lead frame 1404, thus keeping the encapsulating material on one side of the lead frame 1404. The encapsulating material 1504 may then be hardened and cured to form a cap. One cap may be used to cover the entire lead frame. In the alternative, several caps may be provided at various parts of the lead frame.

10 The lead frame 1404, dice 1204, and encapsulating material 1504 may be mounted on sticky tape and then placed on a vacuum chuck of sawing equipment, which is known in the art. The sawing equipment may be used for removing at least part of the connecting sheet 1412 (step 320). In such a removal process, the sawing equipment cuts through only the connecting sheet 1412 between the posts 1408. The removal of at least part of the connecting sheet forms the posts 1408 into lead fingers 1604, which may be electrically isolated from each other, as shown in FIG. 16. If the IC packages have not been singulated, the frame is in one piece held by the encapsulating material 1504, which allows all IC packages to be tested in panel form (step 324). After testing, the saw equipment may then be used to singulate the lead frame 1404, dice 1204, and encapsulating material 1504 into individual IC packages 1608, as shown in FIG. 16. In such a singulation process, the sawing equipment cuts through the encapsulating material 1504.

25 The use of a saw to separate the posts provides the lead fingers 1604 or pins. Such lead fingers or pins are preferable. In the alternative, the connecting sheet may be thinned by etching, as described in U.S. Patent Application Number 09/528,540, entitled "Leadless Packaging Process Using a Conductive Substrate," by Bayan et al., filed March 20, 2000, to provide a leadless IC package.

Since the lead frame 1404 has the same array as described in the previous embodiment, the lead frame 1404 is a universal lead frame, which may accommodate dice of different sizes.

FIG. 17 is a flow chart of a method of making another embodiment of the invention. A lead frame is formed (step 1704). FIG. 18 is a cross-sectional view of part of a lead frame 1800 that may be used in this embodiment of the invention. The lead frame 1800 may be formed by stamping or etching a sheet of metal. The lead frame 1800 provides a plurality of posts 1804. In this embodiment, the posts 1804 all have the same cross-section. In addition, the posts 1804 are spaced apart to form an array. As illustrated, in this embodiment a connecting sheet 1808 extends through the lead frame 1800 so that the connecting sheet 1808 is connected through the center of each of the posts 1804. The connecting sheet 1808 is imperforate in that it extends between the posts 1804 without any apertures. Plating, such as silver plating, may be added to the lead frame 1800.

A plurality of first dice 1812 is attached to the lead frame 1800 (step 1708). A plurality of conductive pads 1816 is on a conductive side of the first dice 1812. The conductive pads 1816 are electrically and mechanically attached to a first set of the plurality of posts 1804 by an electrically conductive adhesive 1820, such as conductive epoxy or solder. Since the conductive pads are held to posts by an electrically conductive adhesive 1820, the conductive side of the first dice 1812 is adjacent to the first set of the plurality of posts 1804 and the conductive side of the dice is the closest part of each die to the posts.

A plurality of second dice 1824 is attached to the plurality of first dice 1812, so that a non-conductive side of each second die 1824 is attached to a non-conductive side of a first die 1812. Conductive pads 1828 on a conductive side of the second dice 1824 are electrically connected to a second set of the plurality of posts 1804 by wire bonds 1832 (step 1716). The non-conductive sides of the first die 1812 are opposite from the conductive sides of the first dice 1812, and the non-conductive sides of the second dice 1824 are opposite from the conductive sides of the second dice 1824, as shown.

The plurality of first dice 1812 and plurality of second dice 1824 may then be encapsulated (step 1720). Using conventional molding the lead frame 1800 can be

encapsulated by an encapsulating material 1836. Since the connecting sheet 1808 is imperforate in that it extends between the posts without any apertures, the connecting sheet prevents the encapsulating material from flowing under the lead frame 1800, thus keeping the encapsulating material 1836 on one side of the lead frame 1800. The  
5 encapsulating material may then be hardened and cured to form a cap. One cap may be used to cover the entire lead frame. In the alternative, several caps may be provided at various parts of the lead frame.

The lead frame 1800 and encapsulating material 1836 may be mounted on sticky tape and then placed on a vacuum chuck of sawing equipment, which is known in the art.  
10 The sawing equipment may be used for removing all or parts of the connecting sheet 1808 (step 1724). In such a removal process, the sawing equipment may cut through only the connecting sheet 1808 between the plurality of posts 1804. The removal of parts of the connecting sheet forms the posts 1804 into lead fingers 1904, which may be electrically isolated from each other, as shown in FIG. 19. Since the IC packages have not been  
15 singulated, the IC packages are in one piece held together by the encapsulating material 1836. This allows all IC packages to be tested in panel form, which allows faster and easier testing. After testing, the saw equipment may then be used to singulate the lead frame 1800 and encapsulating material 1836 into individual IC packages 1908 (step 1728). In such a singulation process, the sawing equipment cuts through the  
20 encapsulating material 1836.

If panel testing is not desired, then the chip scale IC packages may be singulated before electrical testing. In such a case, singulation may be performed during the step of removing the connection sheet. In other embodiments, other methods besides using saw equipment, such as etching or laser cutting, may be used to remove the connection sheet  
25 and/or for singulation.

The resulting IC package is able to package a first die and a second die with a resulting footprint that is slightly larger than one of the dice. By mounting the first die so that the conductive side is facing the lead fingers so that the lead fingers are directly connected to the conductive pads, the lead fingers have a footprint about equal to the  
30 footprint of the first die. In addition, the placement of the conductive side of the first die adjacent to the lead fingers allows the placement of a non-conductive side of the second

die on a non-conductive side of the first die, so that the conductive side of the second die faces away from the lead fingers. The wire bonding of conductive pads of the second die to the second set of lead fingers provides an IC package footprint for two packaged dice, which is about equal to the footprint of a conventional IC package for a single die.

5           In other embodiments, a universal lead frame with an array of posts may have the posts in the array closer together (a smaller distance and pitch between posts) to allow a higher number of contact posts for a particular die size. In another embodiment, a universal lead frame may have a rectangular array where the posts are arranged in columns and rows where the distance between (pitch of) the rows is different than the  
10           distance between the columns. Posts in other embodiments may be rectangular, circular, or have some other cross-section instead of having a square cross-section. The posts may be placed in other patterns, such as in a circular pattern.

          Other methods may be used to bond the die to the lead frame, with these methods preferably providing an electrically conductive mechanical bond between the conductive  
15           pads and the posts.

          While this invention has been described in terms of several preferred embodiments, there are alterations, modifications, permutations, and substitute equivalents which fall within the scope of this invention. It should also be noted that there are many alternative ways of implementing the methods and apparatuses of the present invention. It is therefore intended  
20           that the following appended claims be interpreted as including all such alterations, permutations, and substitute equivalents as fall within the true spirit and scope of the present invention.

**CLAIMS**

What is claimed is:

1. An integrated circuit package, comprising:

a first die with a conductive side;

5 a plurality of lead posts, wherein the conductive side of the first die faces the plurality of lead posts; and

an encapsulating material encapsulating the first die and an end of the lead posts adjacent to the conductive side of the die.

10 2. The integrated circuit package, as recited in claim 1, wherein the conductive side of the first die is mechanically and electrically connected to the plurality of lead posts.

15 3. The integrated circuit package, as recited in claim 2, wherein the conductive side comprises a plurality of spaced apart conductive pads, which are mechanically and electrically connected to the lead posts.

4. The integrated circuit package, as recited in claim 3, wherein the plurality of conductive pads is mechanically and electrically connected to the lead posts by conductive epoxy.

20 5. The integrated circuit package, as recited in claim 2, wherein the lead posts have equal spacing and pitch.

6. The integrated circuit package, as recited in claim 2, wherein the lead posts have a square cross section.

7. The integrated circuit package, as recited in claim 2, wherein the lead posts have a round cross section.

8. The integrated circuit package, as recited in claim 2, wherein the lead posts have lengths which are substantially perpendicular to the conductive side of the first die.

9. The integrated circuit package, as recited in claim 2, further comprising a second die with a conductive side and a side opposite the conductive side, wherein the side opposite the conductive side is connected to a side opposite the conductive side of the first die.

10. The integrated circuit package, as recited in claim 9, further comprising wirebonding connected between the conductive side of the second die and at least one lead post of the plurality of lead posts.

11. A method for packaging integrated circuits, comprising:  
providing a lead frame of a conductive material with a plurality of lead posts and a connecting sheet connecting the plurality of lead posts;

attaching a plurality of first dice to the lead frame, wherein each first die is electrically and mechanically connected to a plurality of the plurality of lead posts, and wherein a conductive side of each first die faces the plurality of lead posts; and

encapsulating the plurality of dice with an encapsulating material.

12. The method, as recited in claim 11, further comprising removing at least part of the connecting sheet to electrically isolate the plurality of lead posts from each other.

5 13. The method, as recited in claim 12, further comprising singulating the encapsulated first dice.

10 14. The method, as recited in claim 13, wherein attaching the plurality of first dice to the lead frame comprises placing a conductive epoxy between conductive pads on the plurality of dice and the plurality of lead posts.

15 15. The method, as recited in claim 14, further comprising testing the integrated circuit packages as a panel before the step of singulation.

20 16. The method, as recited in claim 15, wherein the removing of the connecting sheet forms lead fingers.

25 17. The method, as recited in claim 16, wherein the connecting sheet is an imperforate connecting sheet.

18. The method, as recited in claim 17, wherein the conductive side of a die of the plurality of dice comprises a plurality of spaced apart conductive pads, wherein the conductive epoxy electrically and mechanically connects each conductive pad to a post of the plurality of posts.



19. The method, as recited in claim 18, wherein the placing the conductive epoxy comprises:

placing the conductive epoxy on an upper surface of each of the plurality of posts; and

placing a plurality of dice so that each conductive pad is placed into conductive epoxy  
5 on an upper surface of a post.

20. The method, as recited in claim 13, further comprising:

attaching a plurality of second dice to the plurality of first dice, wherein each second die has a conductive side and a side opposite the conductive side, wherein the side opposite  
10 the conductive side of each second die is connected to a side opposite the conductive side of a first die, wherein each second die has a plurality of conductive pads on the conductive side of the second die; and

wirebonding conductive pads of each second die to lead posts of the plurality of lead posts of the lead frame, wherein encapsulating the plurality of first dice encapsulates the  
15 plurality of second dice.

**CHIP SCALE PIN ARRAY****ABSTRACT**

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An integrated circuit package with lead fingers with a footprint on the order of the integrated circuit footprint is provided. A lead frame may be made from a metal sheet, which may be stamped or etched. The lead frame provides a plurality of posts and a connecting sheet connecting the plurality of posts. Dice are adhesively mounted to the plurality of posts. The dice have a conductive side with a plurality of conducting pads where each conducting pad is electrically and mechanically connected to a post. An encapsulating material is placed over the dice and lead frame, with the connecting sheet keeping the encapsulating material on one side of the lead frame. Parts of the connecting sheet are then removed, electrically isolating the posts. The integrated circuit packages formed by the encapsulated dice and leads may be tested as a panel, before the integrated circuit packages are singulated.

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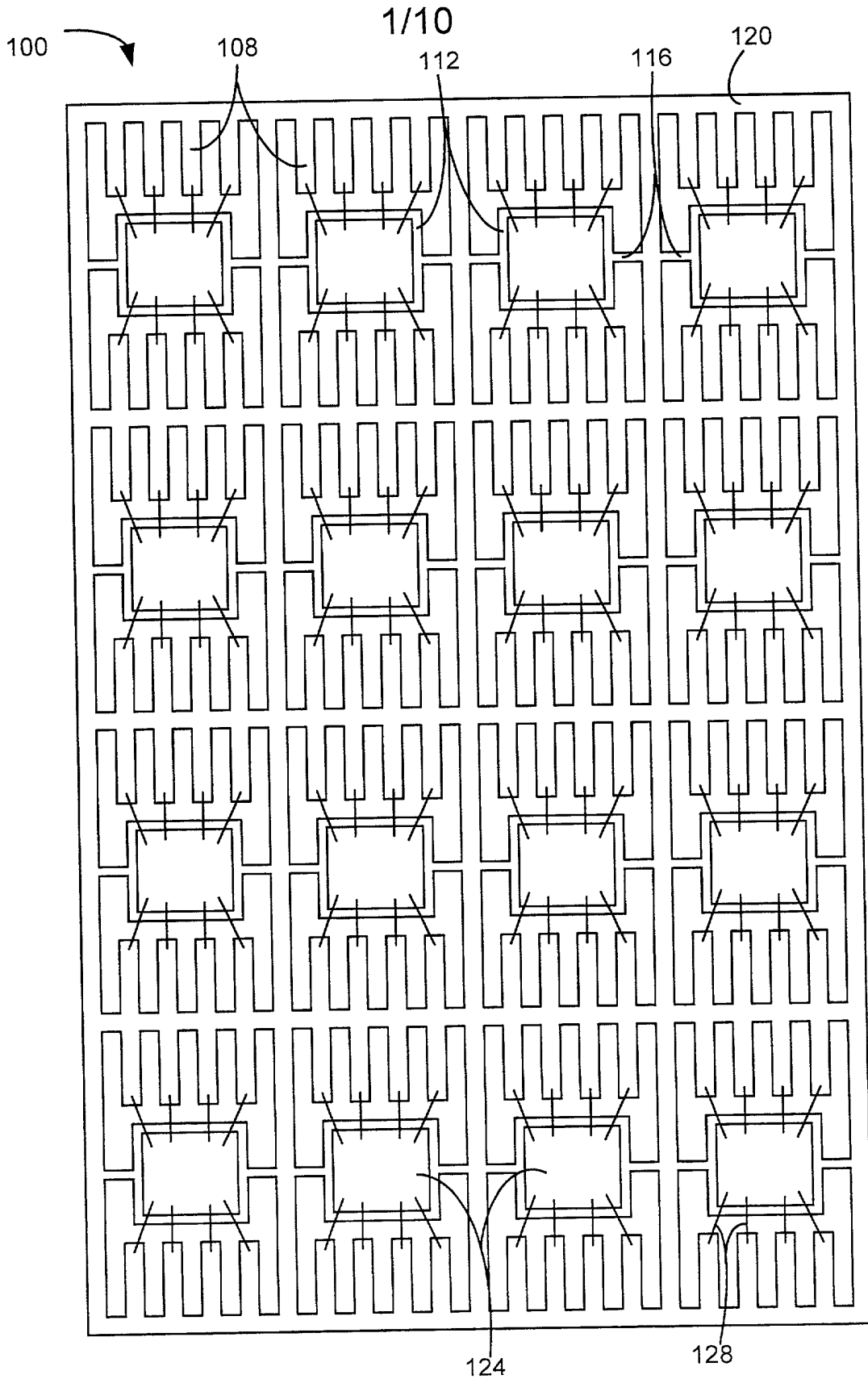


FIG. 1 (PRIOR ART)

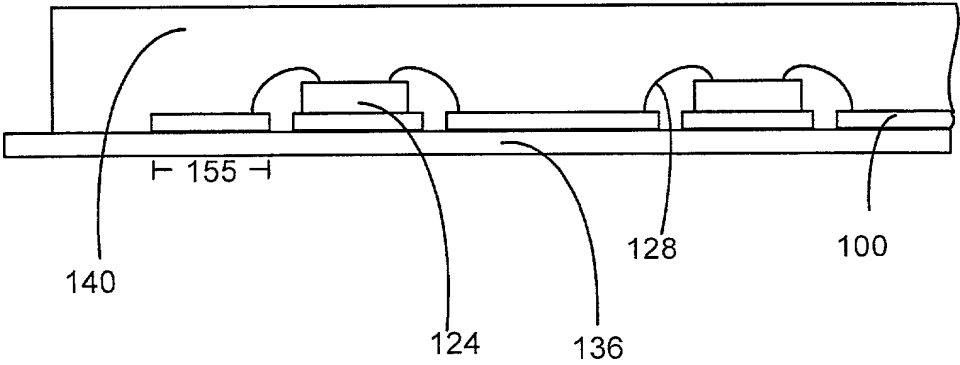


FIG. 2 (PRIOR ART)

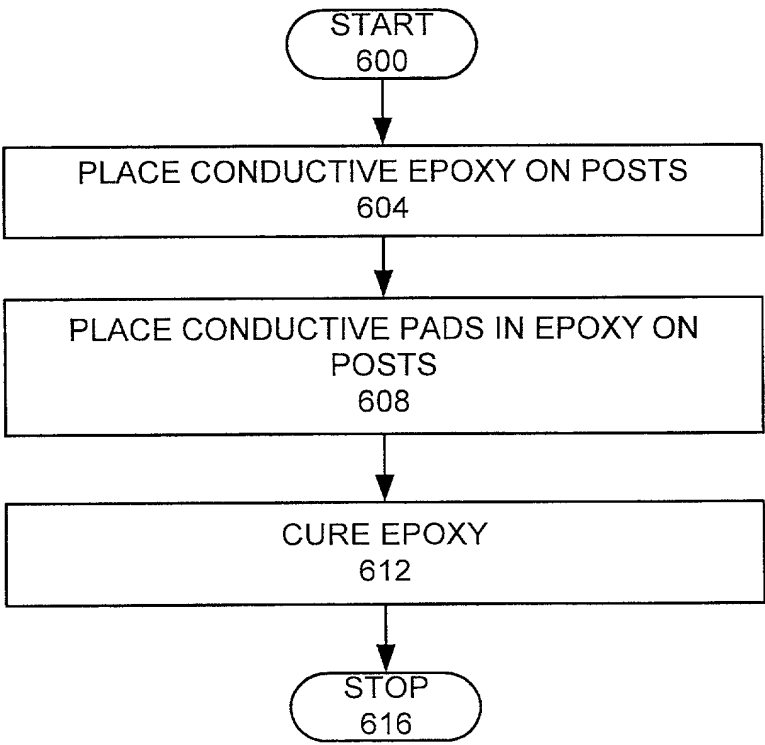


FIG. 6

3/10

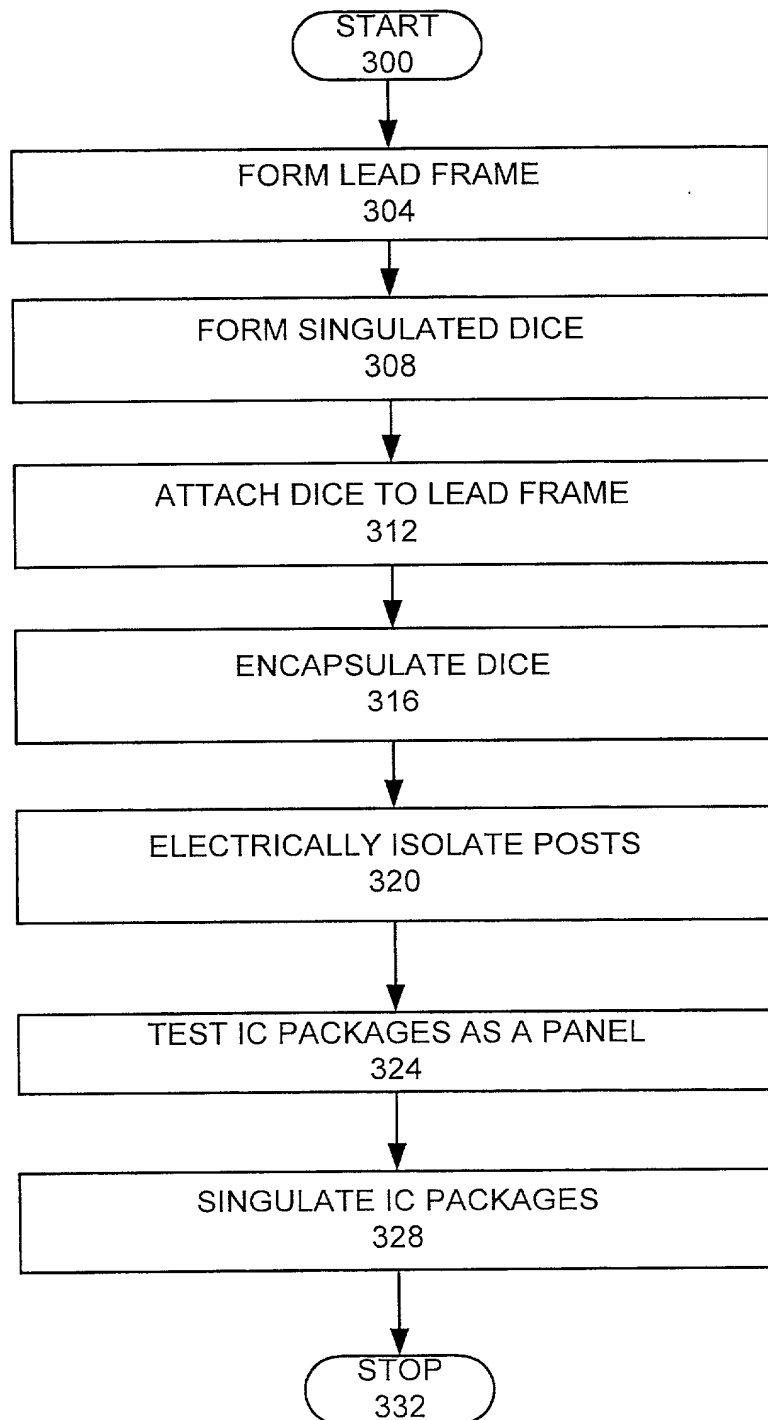
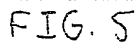
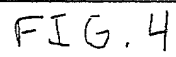
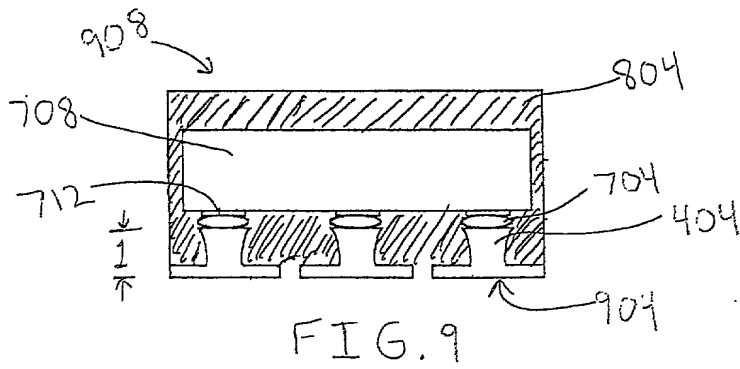
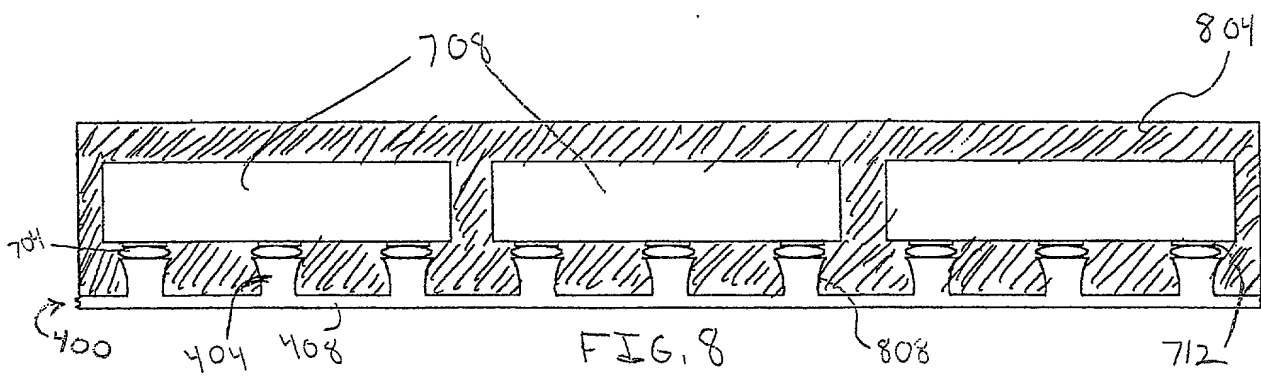
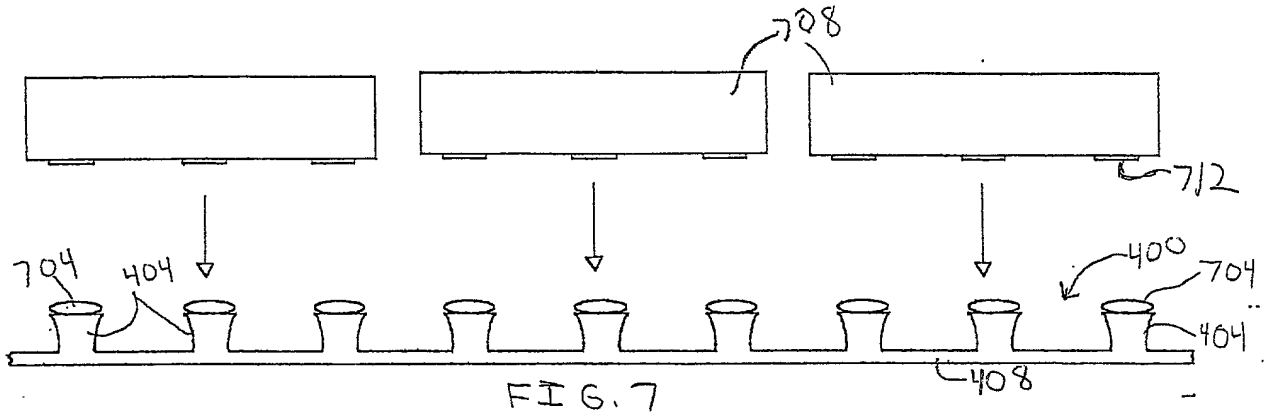


FIG. 3





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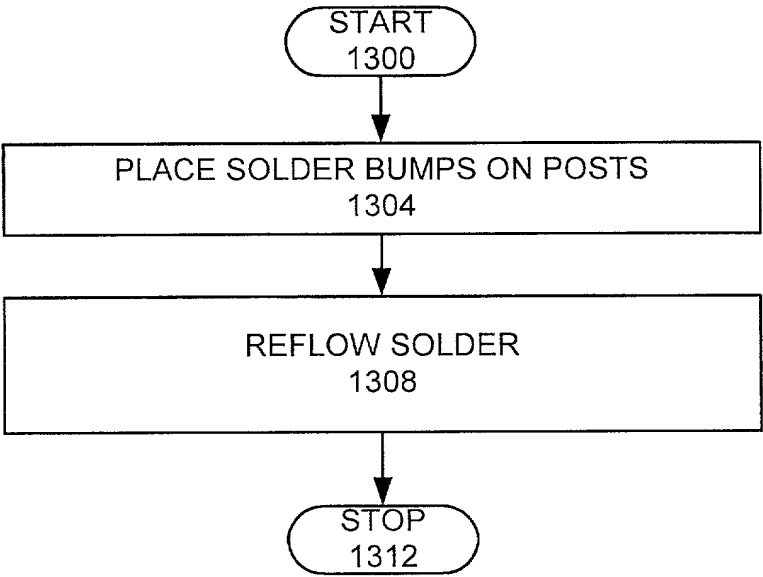
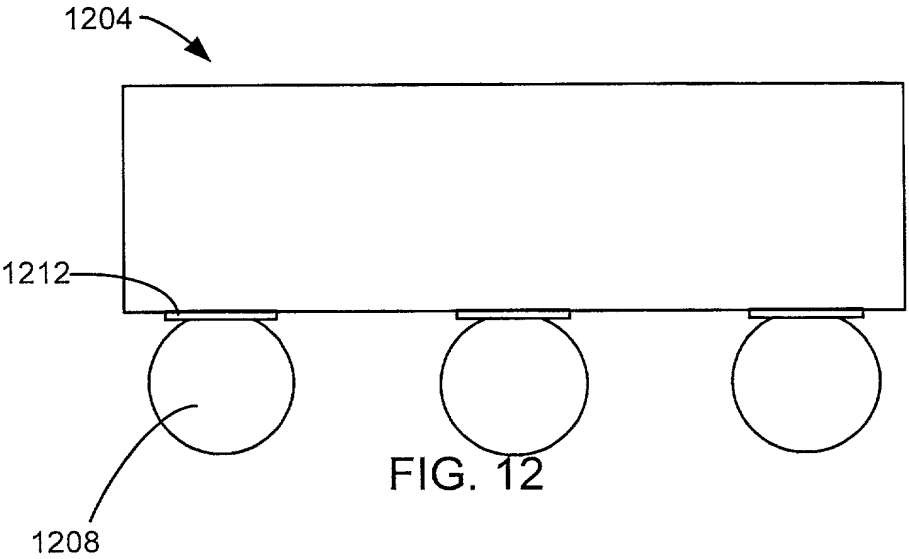
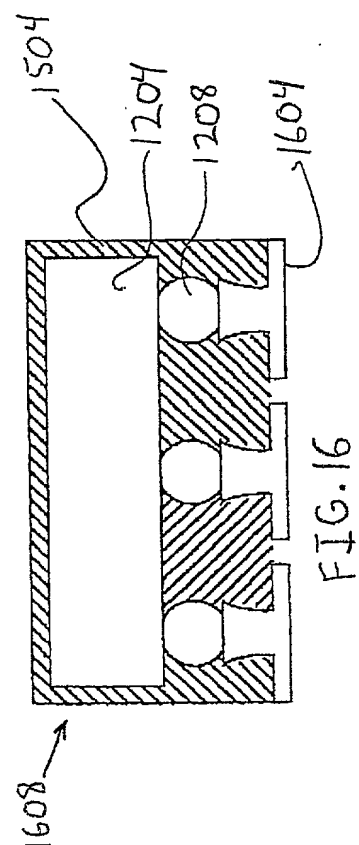
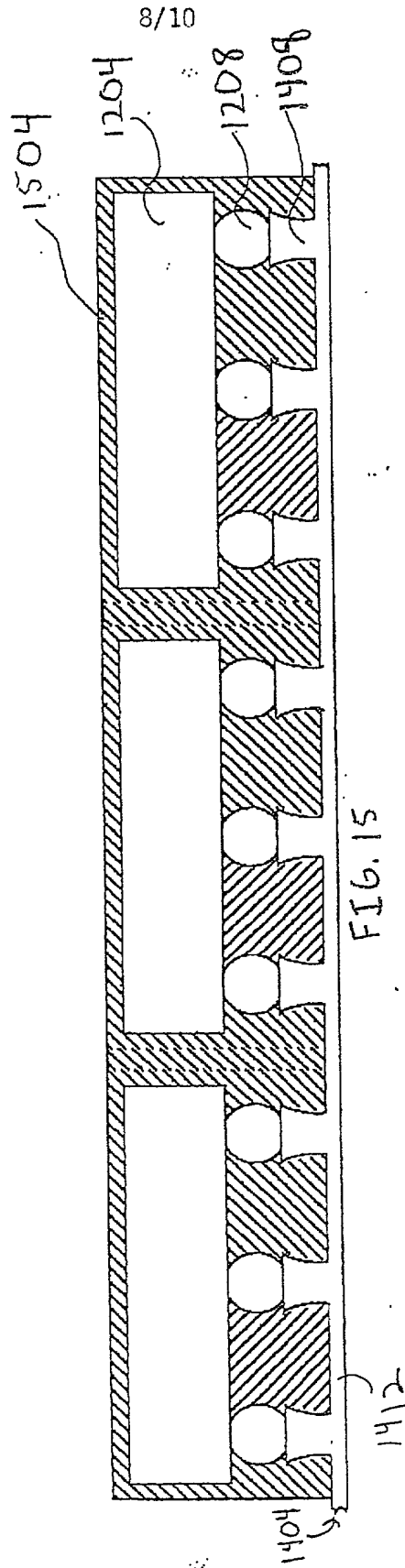
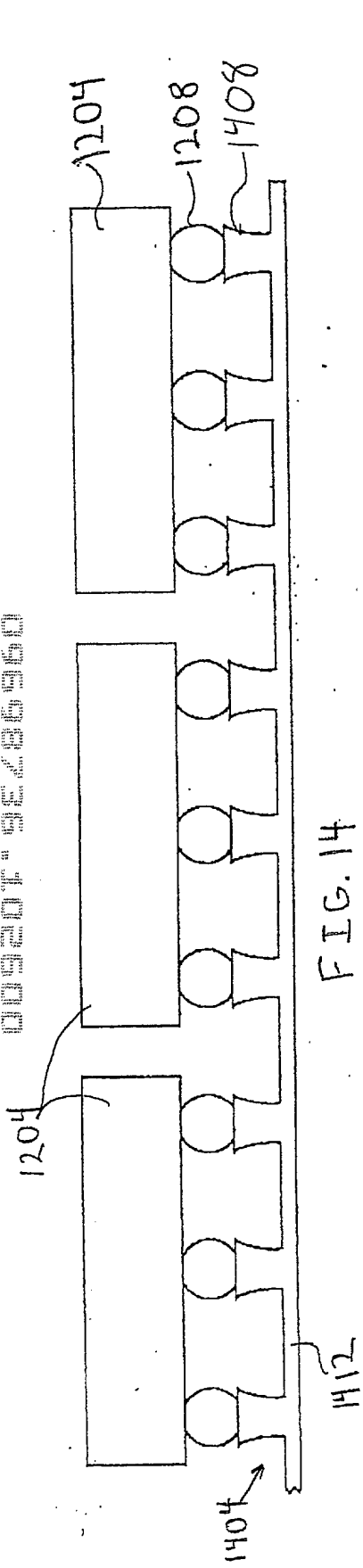


FIG. 13



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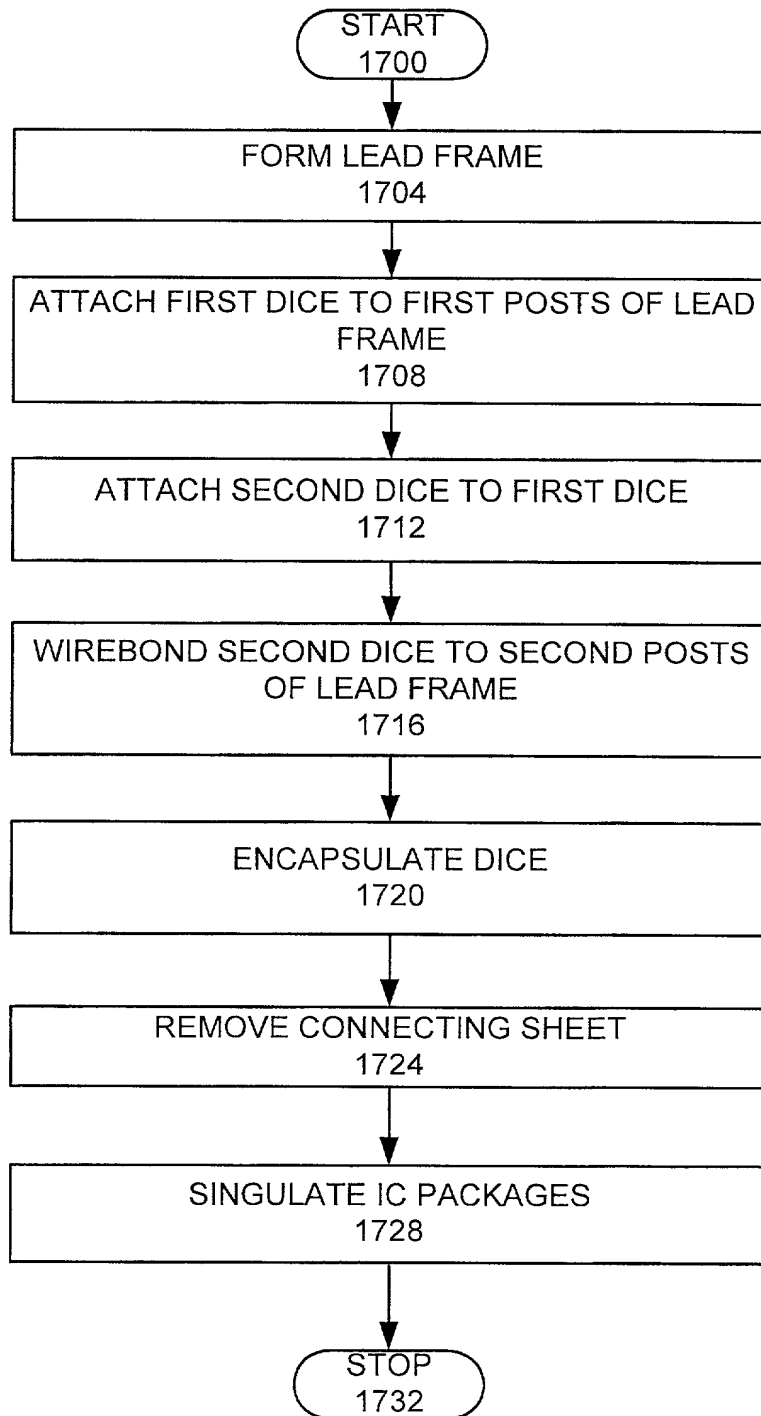
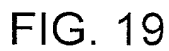


FIG. 17



## DECLARATION AND POWER OF ATTORNEY FOR ORIGINAL U.S. PATENT APPLICATION

Attorney's Docket No. NSC1P194/P04836

As a below-named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe that I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled: CHIP SCALE PIN ARRAY, the specification of which,

- (check one)
1. ☒ is attached hereto.
2. ☐ was filed on \_\_\_\_\_ AS  
U.S. Application No. \_\_\_\_\_  
and was amended on \_\_\_\_\_.
3. ☐ was filed on \_\_\_\_\_ AS  
International PCT Application No. \_\_\_\_\_  
and was amended on \_\_\_\_\_.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, CFR §1.56.

I hereby claim foreign priority benefits under Title 35, United States code, § 119(a)-(d) or § 365(b) of any foreign application(s) for patent or inventor's certificate, or § 365(a) of any PCT International application which designated at least one country other than the United States, listed below and have identified below, by checking the box, any foreign application for patent or inventor's certificate, or PCT International application having a filing date before that of the application on which priority is claimed:

**Prior Foreign Application(s)**

(Appl. No.)	(Country)	(Filing Date)
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### Priority Benefits Claimed?

☐ Yes ☐ No

(Appl. No.)	(Country)	(Filing Date)
-------------	-----------	---------------

☐ Yes ☐ No

**[ ] = 0**

(Appl. No.)	(Country)	(Filing Date)
-------------	-----------	---------------

☐ Yes ☐ No

**[ ]**

I hereby claim the benefit under 35 U.S.C. §119(e) of any United States provisional application(s) listed below:

(Application No.)	(Filing Date)
-------------------	---------------

(Application No.) \_\_\_\_\_ (Filing Date) \_\_\_\_\_

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s), or § 365(c) of any PCT International application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, § 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application:

**Prior U.S. Application(s)**

(Application Serial No.)

(Filing Date)

(Status - patented, pending, abandoned)

(Application Serial No.)

(Filing Date)

(Status - patented, pending, abandoned)

And I hereby appoint the law firm of **Beyer Weaver & Thomas, LLP** and all practitioners who are associated with the Customer Number 022434 as my principal attorneys to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith.

**Direct Correspondence To:**

**Customer Number: 022434**  
**BEYER WEAVER & THOMAS, LLP**  
**P.O. Box 778**  
**Berkeley, CA 94704-0778**



### Direct Telephone Calls To:

**Michael Lee at telephone number (831) 655-2300**

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Typewritten Full Name of

Sole or First Inventor: SHAIHRAM MOSTAFAZADEH

Citizenship: USA

Inventor's signature. Sharon M. Toppo

Date of Signature: 10/25/00

Residence: (City) San Jose

(State/Country) CA/USA

Post Office Address: 4238 Monet Circle, San Jose, CA 95136

Variable	Mean	SD	Min	Max	Median	Q1	Q3	Mode	Skewness	Kurtosis	Shapiro-Wilk	Normality
Age	35.5	10.5	20	65	35	30	40	35	0.1	3.0	0.95	Normal
Gender	1.5	0.5	1	2	1.5	1.5	1.5	1.5	0.0	0.0	0.99	Normal
Marital Status	2.5	1.0	1	4	2.5	2.0	3.0	2.5	0.1	3.0	0.95	Normal
Education	15.5	2.5	10	20	15	14	16	15	0.1	3.0	0.95	Normal
Income	1000	500	500	2000	1000	800	1200	1000	0.1	3.0	0.95	Normal
Occupation	1.5	0.5	1	2	1.5	1.5	1.5	1.5	0.0	0.0	0.99	Normal
Health Status	2.5	1.0	1	4	2.5	2.0	3.0	2.5	0.1	3.0	0.95	Normal
Stress Level	3.5	1.5	1	5	3.5	3.0	4.0	3.5	0.1	3.0	0.95	Normal
Life Satisfaction	4.5	1.0	3	5	4.5	4.0	5.0	4.5	0.1	3.0	0.95	Normal
Resilience	3.5	1.0	1	5	3.5	3.0	4.0	3.5	0.1	3.0	0.95	Normal
Optimism	4.5	1.0	3	5	4.5	4.0	5.0	4.5	0.1	3.0	0.95	Normal
Emotional Stability	3.5	1.0	1	5	3.5	3.0	4.0	3.5	0.1	3.0	0.95	Normal
Self-Esteem	4.5	1.0	3	5	4.5	4.0	5.0	4.5	0.1	3.0	0.95	Normal
Life Satisfaction	4.5	1.0	3	5	4.5	4.0	5.0	4.5	0.1	3.0	0.95	Normal
Resilience	3.5	1.0	1	5	3.5	3.0	4.0	3.5	0.1	3.0	0.95	Normal
Optimism	4.5	1.0	3	5	4.5	4.0	5.0	4.5	0.1	3.0	0.95	Normal
Emotional Stability	3.5	1.0	1	5	3.5	3.0	4.0	3.5	0.1	3.0	0.95	Normal
Self-Esteem	4.5	1.0	3	5	4.5	4.0	5.0	4.5	0.1	3.0	0.95	Normal